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1. This document has been translated by computer. So the translation may not reflect the original precisely.
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3. In the drawings, any words are not translated.

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**CLAIMS**

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**[Claim(s)]**

[Claim 1] The memory slot for equipping with a memory module, and the memory bus which has the signal line connected to the connector terminal of said memory slot, The memory controller which controls the memory chip in the memory module with which said memory slot was equipped through said memory bus, Are the computer system which has electrical load and the module terminal of said memory module is connected to the signal line of said memory bus connected to the connector terminal of said memory slot with which it is equipped with the memory module. The computer system characterized by connecting said electrical load to the signal line of said memory bus connected to the connector terminal of said memory slot with which it is not equipped with a memory module.

[Claim 2] The memory slot for equipping with a memory module, and the memory bus which has the signal line connected to the connector terminal of said memory slot, The memory controller which controls the memory chip in the memory module with which said memory slot was equipped through said memory bus, It is the computer system which has electrical load. In said memory slot It has the 1st connector terminal connected to the signal line of said memory bus, and the 2nd connector terminal connected to said electrical load. In the condition of not being equipped with a memory module, said 1st connector terminal and said 2nd connector terminal are connected electrically. In the condition of having been equipped with the memory module, said 1st connector terminal and said 2nd connector terminal are intercepted. The computer system characterized by equipping the switch connector constituted so that said 1st connector terminal might be connected with the module terminal of a memory module.

[Claim 3] The computer system according to claim 1 or 2 characterized by said electrical load consisting of the series circuit or capacitative element of a resistance element and a capacitative element.

[Claim 4] The computer system according to claim 3 characterized by the resistance of said resistance element and the capacity value of a capacitative element approximating the resistance and capacity value at the time of the actuation seen from the module terminal of said memory module, respectively.

[Claim 5] The computer system according to claim 4 characterized by the resistance of said resistance element and/or the capacity value of a capacitative element being adjustable.

[Claim 6] The switch connector which is a switch connector which changes connection of the signal line of said memory bus in the computer system equipped with the memory slot and electrical load for equipping with a memory bus and a memory module, connects the module terminal of said memory module to the signal line of said memory bus when said memory slot is equipped with the memory module, and connects said electrical load to the signal line of said memory bus when said memory slot is not equipped with the memory module.

[Claim 7] The switch connector according to claim 6 characterized by being prepared in the interior of the case of said memory slot.

[Claim 8] It has the 1st and 2nd metal members. To said 1st metal member the signal line of said memory bus When said electrical load is connected to said 2nd metal member and said memory slot is not equipped with the memory module, the said 1st and 2nd metal member will be in a contact condition mutually. The switch connector according to claim 6 or 7 characterized by the said 1st and 2nd metal member being in a non-contact condition mutually when said memory slot is equipped with the memory module.

[Claim 9] A switch connector given in either of claims 6-8 characterized by said 1st metal member being the cantilever spring with which the end was fixed to the case of said memory slot.

[Claim 10] A switch connector given in either of claims 6-9 to which said 2nd metal member is characterized by being

FROM : SUGHRUE-DC  
JP,2003-085122,A [CLAIMS]

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Oct. 17 2005 02:37PM P4/4

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fixed to the case of said memory slot.

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